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## **CODE NO:- Z-8094**

## FACULTY OF ENGINEERING & Technology

## M.E.(CSE) Year Examination-June-2015 EL-I- Advanced Computer Architecture

(Revised)

Time: Three Hours			Maximum Ma		ks: 80	
			"Please check whether you have got the right question paper."			
			i) Assume suitable data wherever necessary.			
			ii) Answer <u>any three</u> questions from section A & B.			
			iii) Q. No. 5 and Q. No. 6 are compulsory.			
			SECTION-A			
Q.1	a)	-	ed memory organization in detail with suitable diagram.		10	
	b)	Explain diffe	erent approaches for scalability analysis? Explain the principle of scal	able performance.	10	
Q.2	a)	What are the design issues in Arithmetic pipeline design?			10	
	b)	Describe line	ear pipeline processors.		10	
Q.3		Write short n	ote (any two)		20	
		i) Pa	arallel algorithms for array processors			
			uper scalar pipe line design			
			rganization of array processor			
			on linear pipeline processor			
		v) Fl	lynn's classification			
			SECTION-B			
Q.4	a)	What is vector processing? Explain application domain where vector processing is important?		10		
	b)	What is array	processor? How to enhance the performance of array processor?		10	
Q.5	a)	Explain the structure of multiprocessor system?			10	
	b)	Explain sync	hronization mechanism in multiprocessor & multicomputer systems?	<b>)</b>	10	
Q.6		Write short n	note ( <u>any two</u> )		20	
		i) V	LIW Architecture			
			arallel processing models			
		,	IMD machine Illiac –IV			
			ache coherence in multiprocessor system			
		v) V	ector instructions.			