## SUBJECT CODE NO: E-8030 FACULTY OF ENGINEERING AND TECHNOLOGY M.E. (Electrical Power Systems) Examination Nov/Dec 2017 Digital Protection of Power System (Revised)

| [Time: Three Hours] |  | ľ |
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| N.B                 | Please check whether you have got the right question paper.  i) Solve any two questions from each section.  ii) Assume suitable data, draw neat diagram wherever necessary.  Section-A |   |
| Q.1                 | a) Explain induction disc types of over-current relay with neat diagram. What are the disadvantages 10 of induction disc types of relay?   | 0 |
|                     | b) Compare solid state relay with electromagnetic relay. With block diagram explain working of solid state relay.  | 0 |
| Q.2                 | a) Explain solid state protection scheme for transformer with neat diagram.  | 0 |
|                     | b) Discuss the theory and basic principle of digital protection of a transmission line.  | 0 |
| Q.3                 | a) Explain 8085 microprocessor in detail.  | 0 |
|                     | b) Explain microprocessor based percentage differential relay scheme for the protection of a transformer.  | 0 |
|                     | Section = B  |   |
| Q.4                 | a) Explain DSP 320 series IC with its features and basic architecture.   | ) |
| B                   | b) Explain the process of computing Fourier transform in discrete digital domain. 10   | ) |
| Q.5                 | a) Draw and explain PT modeling with reference to standards.   | ) |
|                     | b) Explain multifunction numerical relay. Write advantages and application of numerical relay. 10  | ) |
| Q.6                 | a) Explain over voltage microprocessor based relay. Draw interfacing diagram.  | ) |
|                     | b) Write the advantages of digital protection. What are the requirements of digital protection system?   | ) |