Total No. of Printed Pages:02

[Time: Three Hours]

SUBJECT CODE NO:- H-281 FACULTY OF ENGINEERING AND TECHNOLOGY

S.E. (EEP/EE/EEE)

Analog and Integrated Circuits (REVISED)

[Max. Marks: 80]

N.B	Please check whether you have got the right question paper. 1. Question No.1 and Q. No. 6 are compulsory. 2. Attempt from each section any two Questions from the remaining question. 3. Assume suitable data where necessary. Section A	
Q.1	Answer any five	10
	1. What is peak detector?	
	2. Define CMRR?	
	3. What is BICMOS?	
	4. List the applications of instrumentation amplifier?	
	5. Draw pin diagram of IC 555	
	6. In which two modes IC 555 timer operates?	
	7. What is voltage transfer curve of an op-amp?	
	8. List the ac and dc parameters of op-amp?	
Q.2	a) Explain with circuit diagram concept of virtual short and virtual ground concept?	07
	b) What are the compensation techniques of op-amp. Explain noise and frequency compensation?	08
Q.3	a) What is zero crossing detectors explain with circuit diagram and output wave forms?	07
	b) Explain operation of mostable multivibrator using IC 555. With circuit diagram and output wave forms.	08
Q.4	a) Explain application of op-amp as a differentiator with neat circuit diagram and output wave forms.	07
200	b) Explain working of square wave generator with circuit diagram & output wave forms.	08
Q.5	Write short notes on (any three)	15
	1. Schmitt trigger using op-amp	
	2. Low voltage ac and dc voltmeter	
	3. Precision rectifier	
	4. Instrumentation Amplifier.	
2 10' 20'	80 A NAV 87 AV 57 AV 97 AV 97 AV	

EXAMINATION MAY/JUNE 2018

Section B

Q.6	Solve any five	10
	1. What is band reject-filter?	00000
	2. State applications of PLL?	7,000
	3. Define voltage regulator?	77700
	4. What is PLL?	0, 44, 50
	5. What is switching regulator?	STANDE PE
	6. What is all pass filter?	2017/10/2017
	7. Define frequency response of filter.	820 V. F.V.
	8. List most commonly used active filters?	5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5
Q.7	a) Explain the difference between band-pass and band reject filters?	07
	b) With neat circuit diagram describe the working of high pass active filter?	08
Q.8	a) Explain application of PLL as frequency demodulation?	07
	b) Describe the operating principle of PLL with block diagram.	08
Q.9	a) Describe the function of voltage regulator IC 723 with pin diagram?	07
	b) Explain the circuit diagram of basic op-amp series voltage regulator.	08
Q.10	Write short notes on (any three)	15
	1. Passive filters	
	2. Switching regulator IC78S40	
	3. ICLM565 PLL	
	4. Butterworth filter.	